



## Features

- ◇ The LGE BT136, 4A III & IV quadrants series triacs, which can be operated in III & IV quadrants. As silicon bidirectional device, with NPNPN five-layer structure; singlesided trenching technology, mesa glass passivation process;
- ◇ multi-layer metallized electrode on the back side;
- ◇ High blocking voltage and high temperature stability.

## Applications

The LGE BT136 is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as washing machine, induction motor starting circuits, vacuum cleaners, power tools and other motors such as speed control maker; heating regulation, solid state relay; phases control circuit; AC motor speed regulation, and so on.

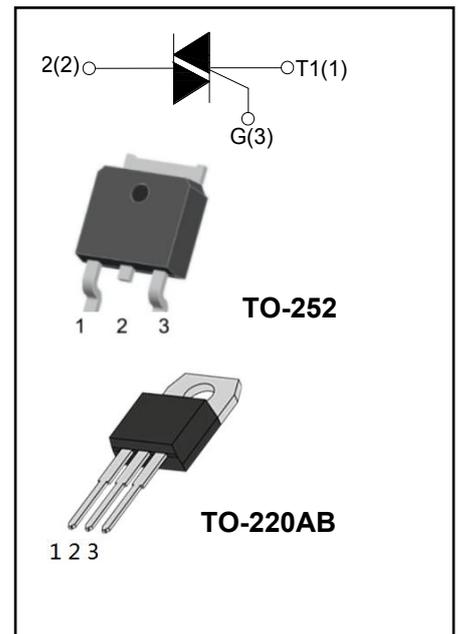
## Mechanical Data

- ◇ Moisture Sensitivity: MSL Level 1, per J-STD-020
- ◇ Terminals: Matte Tin Finish.  
Solderable per MIL-STD-202 Method 208
- ◇ Case Material: Molded Plastic;
- ◇ Molding compound meet UL Flammability Classification Rating 94V-0
- ◇ Case: JEDEC TO-220AB, TO-252

## MAXIMUM RATING

Ratings at 25°C ambient temperature unless otherwise specified.

PARAMETER			SYMBOL	VALUE	Units
R.M.S. On-State Current	Tc=90°C		I <sub>T(RMS)</sub>	4	A
Non-Repetitive Surge Peak On-State Current (f Full Cycle, T <sub>j</sub> initial=25°C)	F=50HZ	T=20ms	I <sub>TSM</sub>	40	A
	F=60HZ	T=16.7ms		41	
I <sup>2</sup> t Value for Fusing	t <sub>p</sub> =10ms		I <sup>2</sup> t	5.1	A <sup>2</sup> s
Critical Rate of Rise of On-State Current I <sub>G</sub> =2×I <sub>GT</sub> , tr≤100ns	F=120HZ	T <sub>j</sub> =125°C	di/dt	50	A/μs
Repetitive peak Off-State voltage	T <sub>j</sub> =25°C		V <sub>DRM</sub> /V <sub>RRM</sub>	500/600/800	V
Non repetitive surge peak off-state voltage	t <sub>p</sub> =10ms	T <sub>j</sub> =25°C	V <sub>DSM</sub> /V <sub>RSM</sub>	V <sub>DRM</sub> /V <sub>RRM</sub> +100	V
Peak Gate Current	t <sub>p</sub> =20μs	T <sub>j</sub> =125°C	I <sub>GM</sub>	4	A
Average Gate Power Dissipation	T <sub>j</sub> =125°C		P <sub>G(AV)</sub>	1	W
Typical Thermal Resistance (Junction to Ambient)	TO-220AB (Non-ins)		R <sub>θJA</sub>	60	°C/W
	TO-252			70	
Typical Thermal Resistance (Junction to Case (AC))	TO-220AB (Non-ins)		R <sub>θJC</sub>	2.4	°C/W
	TO-252			4.2	
Maximum Operating Junction temperature			T <sub>J</sub>	-40~+125	°C
Storage temperature range			T <sub>STG</sub>	-40~+150	°C





**ELECTRICAL CHARACTERISTICS(III QUADRANTS)**

PARAMETER	TEST CONDITIONS	SYMBOL	VALUES				UNIT	
			TW	SW	CW	BW		
Gate Trigger Current	VD=12V,RL=100 Ω ( I II IIIquadrant)	IGT	MAX	5	10	35	50	mA
Gate Trigger Voltage		VGT	MAX	1.5				V
Gate Non-Trigger Voltage	Tj=125°C ( I II IIIquadrant)	VGD	MIN	0.2				V
Holding Current IT=0.5A		IH	MAX	10	15	35	60	mA
Latching Current IG=1.2IGT	I III quadrant	IL	MAX	10	20	50	70	mA
	II quadrant		MAX	15	35	60	80	
Critical Rate of Rise of Off-State Voltage VD=2/3VDRM,Tj=125°C		dv/dt	MIN	20	40	400	1000	V/ μ s
Critical Rate of Rise of Off-State Voltage at Commutation, Tj=125°C		(dv/dt)c	MIN	1.8	2.7	2.5	10	V/ μ s

**ELECTRICAL CHARACTERISTICS(IV QUADRANTS)**

PARAMETER	TEST CONDITIONS		SYMBOL	VALUE				UNIT	
				D	E	F	G		
Gate Trigger Current	VD=12V,RL=100 Ω ( I II IIIIV quadrant)	I II III	IGT	MAX	5	10	25	50	mA
		IV		MAX	10	25	50	100	
Gate Trigger Voltage		All	VGT	MAX	1.3				V
Gate Non-Trigger Voltage	Tj=125°C ( I II IIIquadrant)		VGD	MIN	0.2				V
Holding Current IT=0.5A			IH	MAX	10	15	25	50	mA
Latching Current =1.2IGT	I III IVquadrant	II quadrant	IL	MAX	10	15	40	50	mA
				MAX	15	20	80	100	
Critical Rate of Rise of Off-State Voltage VD=2/3VDRM,Tj=125°C			dv/dt	MIN	10	20	200	400	V/ μ s
Critical Rate of Rise of Off-State Voltage at Commutation, Tj=125°C			(dv/dt)c	MIN	1.8	2.7	2.5	10	V/ μ s

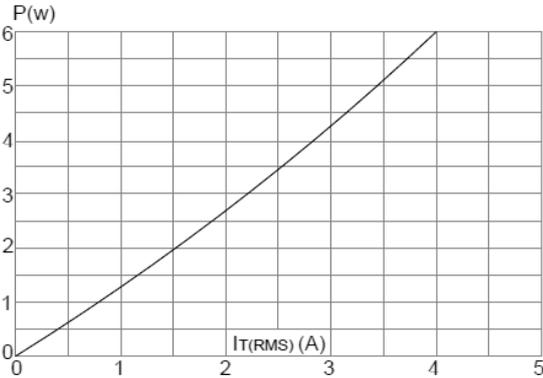
**STATIC CHARACTERISTICS**

PARAMETER	TEST CONDITIONS	SYMBOL		VALUE	UNITS
Peak On-State Voltage	ITM=1 A,Tj=25°C	V <sub>TM</sub>	MAX	1.55	V
Threshold Voltage	Tj=125°C	V <sub>TO</sub>	MAX	0.87	V
Dynamic Resistance	Tj=125°C	R <sub>d</sub>	MAX	120	m Ω
Repetitive Peak Off-Statte Curren V <sub>DRM</sub> =V <sub>R<sub>RM</sub></sub>	Tj=25°C	I <sub>DRM</sub>	MAX	5	μ A
	Tj=125°C	I <sub>R<sub>RM</sub></sub>	MAX	1	mA

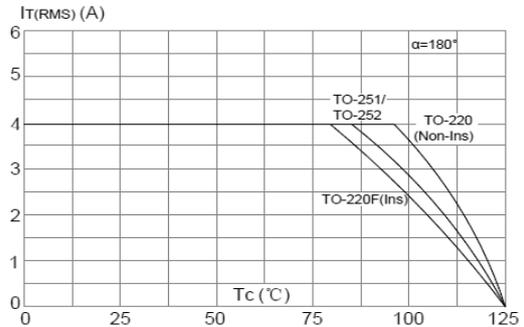


## RATING AND CHARACTERISTICS CURVES

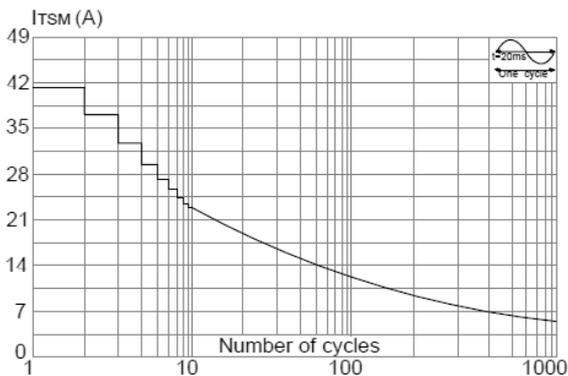
**FIG.1:** Maximum power dissipation versus RMS on-state current



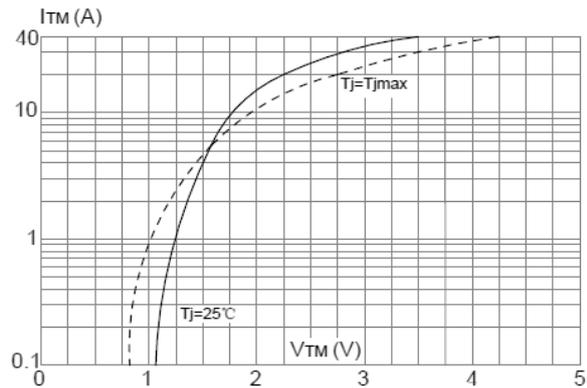
**FIG.2:** RMS on-state current versus case temperature



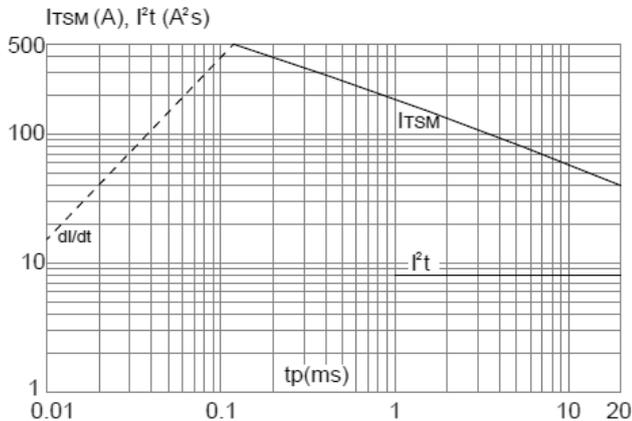
**FIG.3:** Surge peak on-state current versus number of cycles



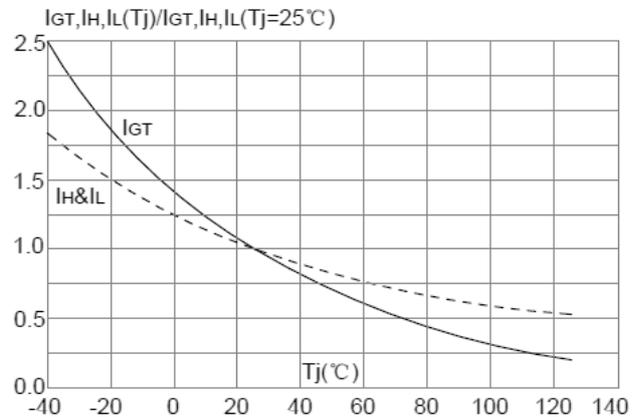
**FIG.4:** On-state characteristics (maximum values)



**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 20\text{ms}$  and corresponding value of  $I^2t$  ( $di/dt < 50\text{A}/\mu\text{s}$ )

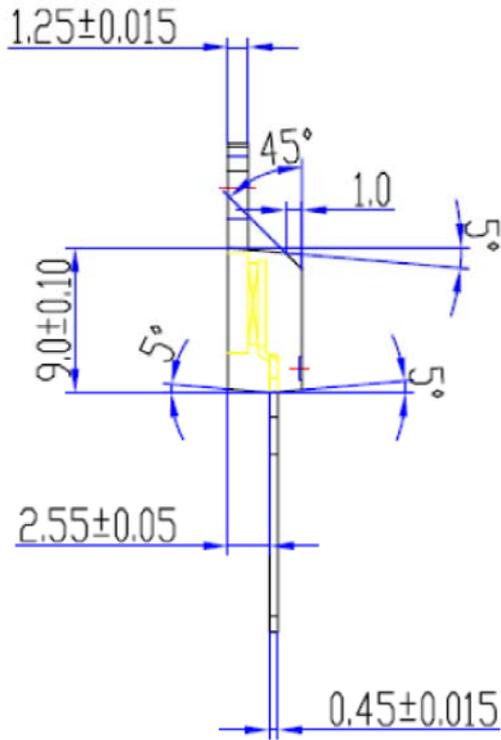
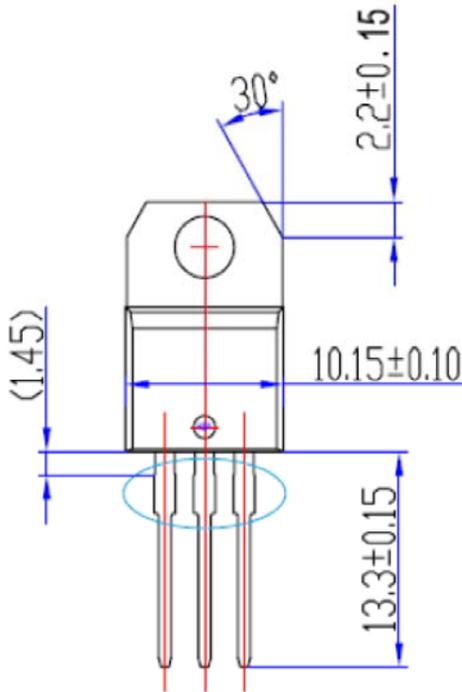


**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature



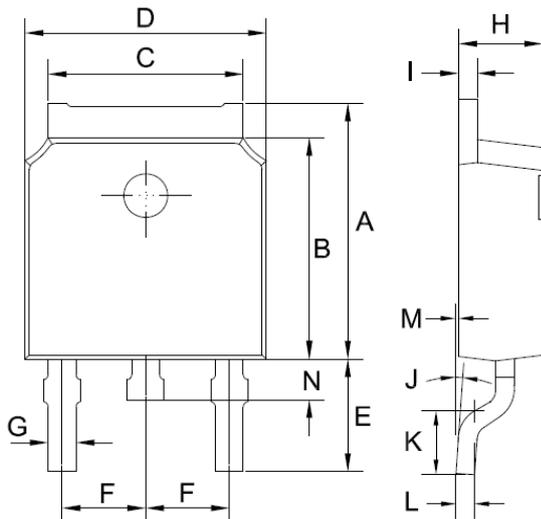
**PACKAGE OUTLINE DIMENSIONS**

**TO-220AB units:mm**



**PACKAGE OUTLINE DIMENSIONS**

**TO-252 units:mm**



TO-252 (DPAK)		
Unit:mm		
DIM	MIN	MAX
A	6.85	7.25
B	5.90	6.30
C	5.13	5.53
D	6.40	6.80
E	2.90	3.30
F	2.19	2.39
G	0.45	0.85
H	2.20	2.40
I	0.41	0.61
J	0°	8°
K	1.45	1.85
L	0.41	0.61
M	0.00	0.12
N	0.60	1.00



## ORDERING INFORMATION

